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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	
				Filing Date	
				First Named Inventor Kalyan Muthukumar	
				Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number 42P18140	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T*
<i>AS</i>	1	Bharadwaj et. al, "The Intel IA-64 Compiler Code Generator", IEEE 0272-1732/00, 2000		
<i>AS</i>	2	Kastner and Winkel, "ILP-based Instruction Scheduling for IA-64", Proceedings of the ACM SIGPLAN Workshop on Languages, Compilers and Tools for Embedded Systems, Utah, 2001, ACM 1-58113-425-8/01/06		
<i>AS</i>	3	K. Cooper et al., "An Experimental Evaluation of List Scheduling", Department of Computer Science, Rice University, Houston, Texas <i>September 1998</i>		
<i>AS</i>	4	Deitrich et. al, "Speculative Hedge: Regulating Compile-Time Speculation Against Profile Variations," Proceedings of the 29th Annual International Symposium on Microarchitecture, December 2-4, pp. 70-79, 1996		

Examiner Signature <i>man Anil</i>	Date Considered <i>02-04-08</i>
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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